

HD74LV1G126A

Bus Buffer Gate with 3-state Output

REJ03D0072-0700 Rev.7.00 Mar 21, 2008

Description

The HD74LV1G126A has a bus buffer gate with 3–state output in a 5 pin package. Output is disabled when the associated output enable (OE) input is low. To ensure the high impedance state during power up or power down, OE should be connected to $V_{\rm CC}$ through a pull-down resistor; the minimum value of the resistor is determined by the current sourcing capability of the driver. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV126A

Supply voltage range: 1.65 to 5.5 V

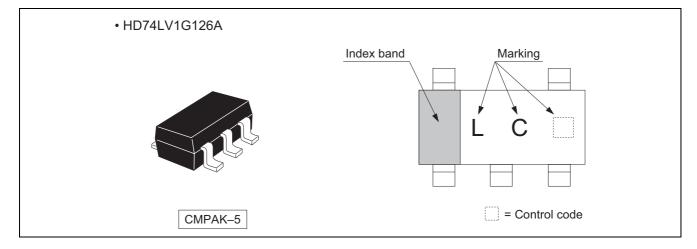
Operating temperature range : -40 to +85°C

- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V) All outputs V_{O} (Max.) = 5.5 V (@ V_{CC} = 0 V, Output : Z)
- Output current ± 6 mA (@V_{CC} = 3.0 V to 3.6 V), ± 12 mA (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

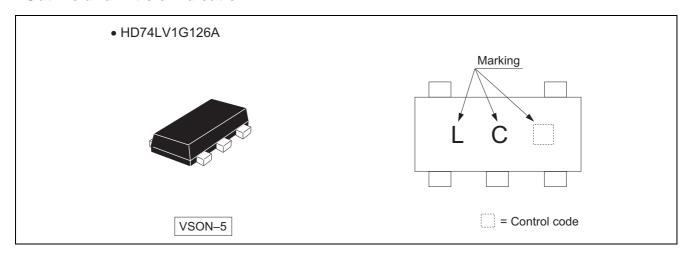
Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1G126ACME	CMPAK-5 pin PTSP0005ZC-A (CMPAK-5V)		CM	E (3000 pcs/reel)
HD74LV1G126AVSE	VSON-5 pin	PUSN0005KA-A (TNP-5DV)	VS	E (3000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Outline and Article Indication



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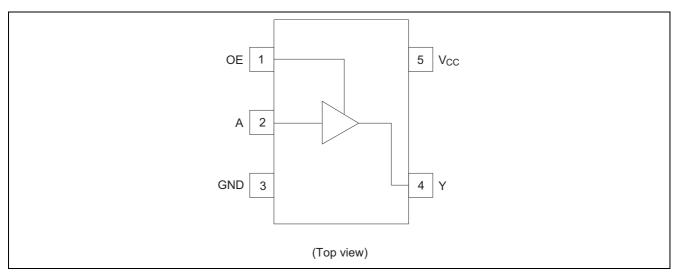


Function Table

Inp	Output Y	
OE	Α	Output 1
Н	Н	Н
Н	L	L
L	X	Z

H : High level
L : Low level
X : Immaterial
Z : High impedance

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{cc}	-0.5 to 7.0	V	
Input voltage range *1	VI	-0.5 to 7.0	V	
Output voltage range *1, 2	Vo	-0.5 to $V_{CC} + 0.5$	V	Output : H or L
Cutput voltage range	V _O	-0.5 to 7.0] v	V _{CC} : OFF or Output : Z
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	I _{OK}	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I _O	±25	mA	$V_O = 0$ to V_{CC}
Continuous current through V _{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) *3	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes:

- The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.
- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	V _{CC}	V	
Output voltage range	VO	0	5.5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	Output : Z
		_	1		V _{CC} = 1.65 to 1.95 V
	I	_	2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
	l _{OL}	_	6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
Output ourrant		_	12	mA	V _{CC} = 4.5 to 5.5 V
Output current	I _{ОН}	_	-1	IIIA	V _{CC} = 1.65 to 1.95 V
		_	-2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		_	-6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		_	-12		V _{CC} = 4.5 to 5.5 V
		0	300		V _{CC} = 1.65 to 1.95 V
Input transition rise or fall rate	A+ / Ax/	0	200	ns / V	$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
Input transition rise or fall rate	Δt / Δv	0	100] 115 / V	V _{CC} = 3.0 to 3.6 V
		0	20		V _{CC} = 4.5 to 5.5 V
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristic

• $Ta = -40 \text{ to } 85^{\circ}\text{C}$

Item	Symbol	V _{CC} (V) *	Min	Тур	Max	Unit	Test condition
		1.65 to 1.95	V _{CC} ×0.75	_	_		
	V _{IH}	2.3 to 2.7	V _{CC} ×0.7	_	_		
	VIH	3.0 to 3.6	V _{CC} ×0.7	_	_		
Input valtage		4.5 to 5.5	V _{CC} ×0.7	_	_	V	
Input voltage		1.65 to 1.95	_	_	V _{CC} ×0.25	V	
	V_{IL}	2.3 to 2.7	_	_	V _{CC} ×0.3		
	VIL	3.0 to 3.6	_	_	V _{CC} ×0.3		
		4.5 to 5.5	_	_	V _{CC} ×0.3		
		1.8	_	0.25	_		
Llyatarasia valtara	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	2.5	_	0.30	_	V	$V_T^+ - V_T^-$
Hysteresis voltage	V _H	3.3	_	0.35	_	V	VT - VT
		5.0	_	0.45	_		
		Min to Max	V _{CC} -0.1	_	_		$I_{OH} = -50 \mu A$
		1.65	1.4	_	_		$I_{OH} = -1 \text{ mA}$
	V _{OH}	2.3	2.0	_	_		$I_{OH} = -2 \text{ mA}$
		3.0	2.48	_	_	V	$I_{OH} = -6 \text{ mA}$
Output voltage		4.5	3.8	_	_		I _{OH} = −12 mA
Output voitage		Min to Max	_	_	0.1		$I_{OL} = 50 \mu A$
		1.65	_	_	0.3		I _{OL} = 1 mA
	V_{OL}	2.3	_	_	0.4		I _{OL} = 2 mA
		3.0	_	_	0.44		I _{OL} = 6 mA
		4.5	_	_	0.55		I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Off state output current	l _{OZ}	Min to Max	_	_	±5	μΑ	V _O = 5.5 V or GND
Quiescent supply current	I _{CC}	5.5	_	_	10	μΑ	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
Output leakage current	I _{OFF}	0	_	_	5	μΑ	V_{IN} or $V_O = 0$ to 5.5 V
Input capacitance	C _{IN}	3.3	_	3.0	_	pF	V _{IN} = V _{CC} or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

• $V_{CC} = 1.8 \pm 0.15 \text{ V}$

Itom	Item Symbol		Ta = 25°C			Ta = -40 to 85°C		Test	FROM	ТО
item Symb	Syllibol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	13.5	23.5	1.0	26.0	20	$C_L = 15 pF$	Α	Y
delay time	t _{PHL}	_	19.0	33.0	1.0	36.0	ns	$C_L = 50 pF$	^	
Enable time	t _{ZH}	_	13.7	26.5	1.0	29.0	ne	$C_L = 15 pF$	OE	V
Enable time	t_{ZL}	_	20.5	36.0	1.0	38.0	ns	$C_L = 50 pF$	OL	r
Disable time	t _{HZ}	_	8.3	20.0	1.0	22.5	no	$C_L = 15 pF$	OE	V
Disable tille	t_{LZ}	_	13.0	29.5	1.0	32.0	ns	$C_L = 50 pF$		'

$\bullet \quad V_{CC} = 2.5 \pm 0.2 \ V$

Item	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test	FROM	ТО
item	Syllibol	Min	Тур	Max	Min	Max	Offic	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	7.1	13.0	1.0	15.5		$C_L = 15 pF$	Α	Y
delay time	t _{PHL}	_	9.2	16.5	1.0	18.5	ns	C _L = 50 pF	A	
Enable time	t _{ZH}	_	7.4	13.0	1.0	15.5	no	$C_L = 15 pF$	OE	V
Enable time	t_{ZL}	_	9.5	16.5	1.0	18.5	ns	$C_L = 50 pF$	OE	Y
Disable time	t _{HZ}	_	5.7	14.7	1.0	17.0	no	$C_L = 15 pF$	OE	V
Disable liffle	t_{LZ}	_	8.1	18.2	1.0	20.5	ns	C _L = 50 pF	OE	Y

• $V_{CC} = 3.3 \pm 0.3 \text{ V}$

ltem	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test	FROM	ТО
item	Syllibol	Min	Тур	Max	Min	Max	Oilit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	5.0	8.0	1.0	9.5	ne	$C_L = 15 pF$	А	Y
delay time	t _{PHL}	_	6.4	11.5	1.0	13.0	ns	$C_L = 50 pF$	^	
Enable time	t _{ZH}	_	5.1	8.0	1.0	9.5	ne	$C_L = 15 pF$	OE	>
Enable time	t_{ZL}	_	6.6	11.5	1.0	13.0	ns	$C_L = 50 pF$	OE	Y
Disable time	t _{HZ}	_	4.4	9.7	1.0	11.5	ne	$C_L = 15 pF$	OE	>
Disable time	t_{LZ}	_	6.1	13.2	1.0	15.0	ns	C _L = 50 pF	OE	ſ

$\bullet \quad V_{CC} = 5.0 \pm 0.5 \ V$

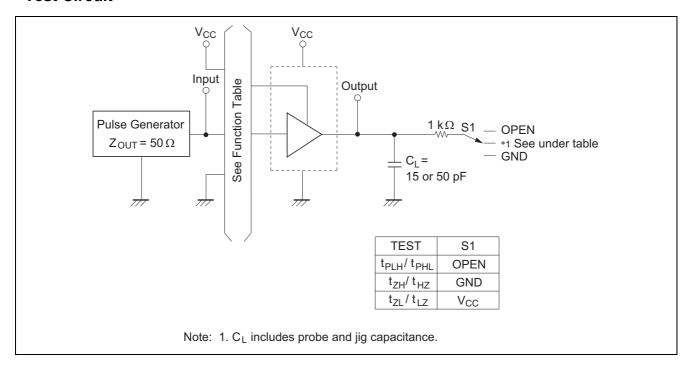
Item	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test	FROM	ТО
item	Syllibol	Min	Тур	Max	Min	Max	Onic	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	3.5	5.5	1.0	6.5	ne l	$C_L = 15 pF$	Α	Y
delay time	t _{PHL}	_	4.6	7.5	1.0	8.5		$C_L = 50 pF$	A	
Enable time	t _{ZH}	_	3.6	5.1	1.0	6.0	ne	$C_L = 15 pF$	OE	Y
Enable time	t _{ZL}	_	4.6	7.1	1.0	8.0	ns	$C_L = 50 pF$	OL	
Disable time	t _{HZ}	_	3.3	6.8	1.0	8.0	ne	C _L = 15 pF OE		
Disable time	t_{LZ}		4.3	8.8	1.0	10.0	ns	$C_L = 50 \text{ pF}$	5	I

Operating Characteristics

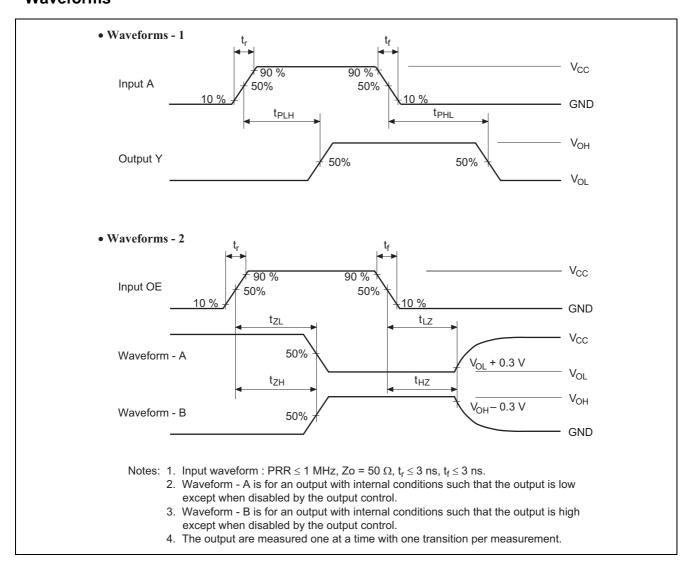
• $C_L = 50 pF$

Item	Symbol	V _{cc} (V)		Ta = 25°C		Unit	Test Conditions	
iteiii	Syllibol	ACC (A)	Min	Тур	Max	Oilit	rest Conditions	
Power dissipation	C	3.3	_	10.5	_	pF	f = 10 MHz	
capacitance	C_{PD}	5.0		11.5	_			

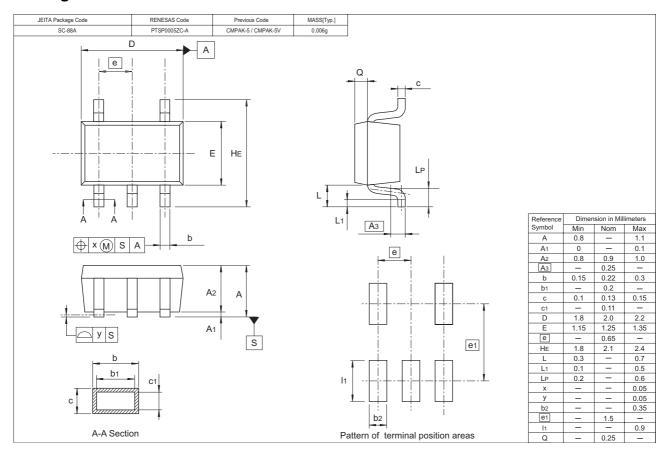
Test Circuit

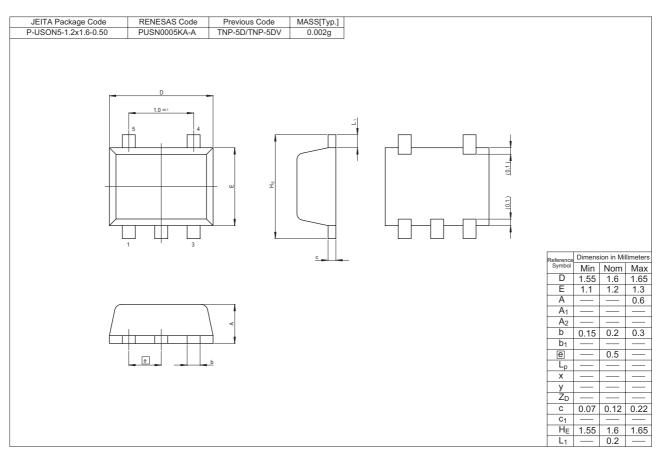


Waveforms



Package Dimensions





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